List of Experiments

Sr. No.	Title
1	Installation & configure DOS, MASM, Debug & X86 mode.
2	Implementation of various ALU operations (ADD, SUB, MUL, DIV, AND, OR, XOR, NOT) through assembly language programming for 8086 using MASM and Debug.
3	Block Transfer and Block Exchange using Index Registers.
4	Displaying 8086 processor's Flag register content on monitor.
5	Implementation of two 8-bit BCD additions with accepting input from keyboard and displaying output on monitor using INT 21H interrupts.
6	Designing 4X4 memory using 1X1 memory chips. Use COA virtual lab by IIT Kharagpur.
7	Implement Booth's Multiplication Algorithm
8	Implement Division Algorithm (Non-Restoring and/or Restoring)
9	Implement various String Operations in 8086 through the utilities provided by DOS and BIOS interrupts (MASM)
10	Implementation of cursor activity like hiding cursor and changing it to box size using INT 10H interrupts.
11	Implementation of number conversion (HEX to BCD, ASCII to BCD, BCD to ASCII) using MASM.
12	Implement file operations [DOS Interrupts in C/MASM]